

**AMENDMENT TO THE SPECIFICATION**

Please replace paragraph [0018] with the following paragraph:

**[0018]** To illustrate operation within the switch matrix of **FIG 4.**, one switch, **S6'**, is shown as closed. When **Row\_1** is driven low by a processor **20** coupled to the current sensing circuit, current flows through **R6** and **R18**, as well as the base to emitter junction of **Q2**, which is the forward current path indicated by the arrow of **Q2**, as is well appreciated by those skilled in the art. Note that since only **Row\_1** is driven low, only the switches on **Row\_1** (**S5'**, **S6'**, **S7'**, or **S8'**, **S1'**, **S2'**, **S3'**, or **S4'**) can switch on the transistors **Q1** through **Q4**.